B. N. Mandal University, Laloonagar, Madhepura

Details of theory & Sessional Papers code of 3rd Year B. Tech. Course

Branch: Electronics & Communication Engineering

Subject	Subject	Branch Code	L	T	P	Th. Ext.	Th. Int.	Sessional
Microprocessor & Its	Code µP , ≪	EC-301	2	1	3	70	30	Microprocessor & Its Applications-100
Applications	AF (EC-302	2	1	3	70	30	Analog Electronics-100
Analog Electronics	AE NT X.	EC-303	7	1	3	70	30	Network Theory-50
Network Theory Flectromagnetic	EMFT .	EC-304	3	1	0	7()	3.0	
Lield Theory							i	
Signals & Systems	SS	EC-305	1 3	1	0	70	30	
Electrical Instruments	EIM /	EC-306	2,	1	3	70	30	Electrical Instruments & Measurements-50
& Measurements		EC 207	13	-	0	70	30	
Elective-I (Introduction to ./.	Elective-I (CS)	EC-307	3	1	0	/0		
Communication								
Systems)				-			20	TE1 .: 11 .50
Elective-II	Elective-II	EC-308	2	1	3	70	30	Elective-II- 50
(Liber Optic	(FOC)			i	-		İ	(Fiber Optic
Communication)	1			-	1			Communication)
Industrial Training	IT	EC-309	0	0	3			Industrial Training-50

Introduction to Communication Systems. Microelectronics IC Design & Fabrication. TV Engineering.

Microcontrollers.

Liber Optic Communication! Digital System Design Imbedded System Design. Divital Image Processing.

Member Secretary

Syllabus Committee

BNMU, Madhepura

(Internal) Faculty of Science & Engineering MIT, Purnea
Name of Virtual (BNMU, Madhepura
Designation: HOD ECE

Address: MIT, RYNER

VINCENTIA # ALL

t: Microprocessor & its Applications Branch Code: EC-301 (ECE/CSE/EE)

2-1-3 crm Intel 8085 Introduction: CPU, register, memory, buses, memory addressing capacity of a CPU. Lecture:3 CPU Architecture: Pin configuration, instructions, addressing modes, instruction word size, Lecture:4 languages. 3. Timing diagram: Read cycle, write cycle, fetch cycle, memory read, memory write, I/O cycle, Lecture:4 4. Programming: Simple programming: 8-bit addition & subtraction, 16-bit addition, delay subroutine using register, finding lowest and highest no. in data array Lecture:5 5. Data transfer schemes, I/O port. Lecture:6 d Term 6. 8255, 8251, 8253, 8257 chips, pin diagram. function of different modes. Lecture:7 7. Interfacing of ADC, analog multiplexer simple and hold. Lecture:4 Intel 8086 6. Architecture: BiU and execution unit, pin diagram, function of different modes Lecture: 4 Lecture: 4 9. Addressing modes: Instruction. Lecture:3 10. Programming. books: 1. Fundamental of Microprocessor & microcontroller by B Ram, Dhanpat Rai. Advance Microprocessor by B Ram. rence books: Microprocessor and interfacing by D V Hall, TMH. Microprocessor Architecture by R S Gaonkar. Microprocessor with application in process control by S I Ahson, TMH. 4. Programming Microprocessor interfaces by Michael Andrews, PHI. The intel Microprocessor Architecture, programming and interfacing by B Brey, PHI. roprocessor Lab: 1. Different programs related to 8085 & 8086 Application of different interfaces. VIVEN 13/1/12 ect; Analog Electronics Branch Code: EC-302

(ECE/EE)

P:-4-1-3

Term

1 Four ideal amplifiers: Ideal voltage amplifier, ideal current amplifier, ideal transconductance amplifier, and ideal transresistance amplifier, distortions (amplitude or harmonic distortions, frequency distortion and phase distortion). Lecture: 4

2. Mid frequency amplifiers:

Analysis of CB, CE & CC amplifiers using hybrid model.

Low and High frequency analysis of CB. CE & CC.

Rise time method for determination of fb using the formula for tr fh - 0.35 and 10% sag method for the determination of flower using sag method. Lecture: 16

3. Bootstrapping in emitter follower: Darlington pair, cascade amplifier, CC-CB Cascade.

Lecture: 5

ond Term

- 4. Multistage amplifiers and band width shrinkage in multistage amplifiers. Lecture: 3
- 5. Incremental model of FET and incremental analysis of common source at low and high Lecture: 3
- 6. Noise and noise figure in amplifiers: Thermal noise, shot noise, flicker noise, friss formula. Lecture: 4

- 7. Class A, Class B, and Class AB power amplifiers with reference to complementary symmetry Lecture: 5 amplifiers, Push Pull amplifier.
- 8. Barkhausen criteria and oscillator: Wein bridge, RC phase shift, quadrature, Hartley, colpitts Lecture: 6 oscillators.
- Lecture: 4 9. Tuned amplifiers: Single tuned amplifiers.

at books:

1. Micro electronics by Millman and Grabel, McGraw Hill. Integrated Electronics by Millman & Halkias, McGraw Hill.

derence:

- 1. Microelectronics circuits by Sedra and Smith, Oxford university.
- Microelectronics circuit analysis and design by Rashid, PWS publication.
- 3 Semiconductor circuit application- an introduction to transistors and IC's by Malvino, TMH.
- 1 Electronic devices and integrated circuit- B P Singh and Rekha Singh, Pearson.
- 5 Electronic principles, 7th edition by Albert Malvino & Davis J Bates, TMH.
- A Hand book of Electronics by Gupta & Kumar, Meerut Pub.

natog Flectronics Lab:

- Two stage RC-coupled amplifier.
- Characteristics (Characteristics) (Characteristi
- Wem bridge oscillator.
- Colputs oscillator.
- Hatley oscillator
- Multistage amplifier.
- 7. Operational amplifier.

etwork Theory

Branch Code: EC-303

(ECE/EE)

ransient response of RC, RL, RLC Circuits to various excitation signals such as step, ramp, inpulse and sinusoidal excitations using Laplace transform. Lerminal pairs or ports: Network functions of one port and two port networks, poles and zeros bt network functions, restrictions on pole and zero locations for driving point functions and ransfer functions, time domain behavior from the pole zero plot. Relationship of two port variables: Short circuit admittance parameters, open circuit Impedance parameters, transmission parameters, hybrid parameters, relationships between parameter sets, inter connection of two port networks.

Principles of network topology: Graph matrices, network analysis using graph theory.

Lecture: 8

Filter fundamentals: High pass, low pass, band pass, and band reject filters Lecture: 8 Positive real functions: Synthesis of one port and two port networks, elementary ideas of active networks.

Networks and systems by D Roy Choudhary New age international.

Network analysis by Van Valkenburg, PHL

. Introduction to modern network synthesis by Van Valkenburg, John Wiley,

ce books:

. Basic circuit theory by Dasoer Kuh, McGraw Hill.

2. A course in electrical circuit analysis by Soni and Gupta, Dhanpat rai & sons.

8. Circuit analysis by G K Mittal, Khanna pub.

k Theory Lab:

on Syllabu Practical based on Syllabus. 19/11/12 XIVENTO 13/11/13

t: Fleetromagnetic Field Theory Branch Code: EC-304

(ECE/EE)

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1 Introduction to field co-ordinate systems.

Lecture: 3

- Hectrostatics: Coulomb's law, Gauss's law and its applications, the potential functions, Equipotential surface, Poisson's and Laplace's equation, applications (solution for some simple cases), capacitances, electrostatics energy, conductor properties and boundary conditions b/w dielectric and dielectric-conductor, uniqueness theorems.

 Lecture: 9
- 3. Magneto statics: Biot-savart law, Ampere's circuital law, Curl, Divergence, Stoke's theorem, Magnetic flux, and magnetic flux density, energy stored in magnetic field, Ampere's force law, Magnetic vector potential, analogy b w electric and magnetic field.

 Lecture: 7
- 4. Maxwell's equations: Equation of continuity for time-varying field, inconsistency of amperecircuital law, Maxwell's equations in differential and integral form.

 Lecture: 4

d Term

- 5. Electromagnetic wave: Solution of wave equation in free space, uniform plane wave propagation, uniform plane waves, the wave equation for conducting medium, wave propagation in lossless medium and inconductive medium, conductors and dielectrics, polarization.
 - Lecture: 4
- 6. Reflection and refractions: Reflection by a perfect conductor with normal as well as oblique incidence. Reflection refraction by perfect dielectrics with normal and oblique incidence. Surface impedance.
- 7. Poynting vector: Poynting theorem, instantaneous, average and complex pointing vector, power loss in a plane conductor.

 Lecture: 3
- 8. Transmission lines: Transmission line theory, low loss radio-frequency and UHF transmission line. UHF line as a transformer, voltage step up of the quarter wave transformer. Transmission line chart (Smith chart).

 Lecture: 10

rooks:

1. Electromagnetic waves and radiating system by E C Jordan, K G Balmain, Pearson.

2 Engineering Electromagnetics by W.H. Hayt, TMH.

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7/12

nals & Systems

Branch Code: EC-305

(ECF E.E.)

Stem and Signal: Definition, classification of systems, standard test signal, properties of radogous System: Force, voltage analogy, force current as a sgy, mechanical coupling devices. aplace transformation: Laplace transform of some important functions, shift theorem and its pplications, laplace transform of periodic functions, analysis of response, initial and final values heorem, response to periodic sinusoidal excitation.

uries methods: Fourier series expansion of periodic functional symmetry condition, exponential form of fourier series fourier integral and fourier transform, analysis by

Z transformation: Z transform, discrete time, L11 system, solution of difference equation, applications of Z transform to open loop system.

analysis of linear system by D K Cheng, Narosa pub.

Modeling and analysis of linear system by J.P. Tiwari, Dhanpat Rai & Sons.

e books:

Signal & system by H P Hus. TMH. signal esystem by 1 Jet. at. TMH.

al exposum by 1 Fet. at. TMIL.

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Figure 13

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t: Hectrical Instruments & Measurements Branch Code: EC-306 (F(T.T.1)

crm

- 1. Measurements of voltage, current, power and power factor, energy and frequency. Lecture: 10 2. Range extension including current and potential transformer. Lecture: 8
- 3. Galvanometer: Dynamics of D'Arsonval Galvanometer, vibration galvanometer, ballistic galvanometer.

Term

- 4. Measurement of inductance and capacitance: AC bridges. Maxwell, Wein, Anderson and shearing bridges, general equations and vector diagram under balanced conditions, error and precaution in bridge measurements, wagner's earth connection and shielding of bridge measurement and elements. Lecture: 10
- 5. Standard AC and DC potentiometers, principle, standardization and application. Lecture: 5
- 6. Measurement of resistances: Measurements of low resistance by Kelvin double bridge and potentiometer method, measurement of high resistance by loss of change method. Lecture: 4 Lecture: 5
- 7. Digital measurements.

ooks:

- 1. Electrical measurement and measuring instruments by E W Golding.
- 2. Basic electrical measurement by M B Stout, PHI.

nce books:

1. Measurement systems: Application and Design by Doebein, 5th edition, TMH.

ical Instruments & Measurements Lab:

Practical based on Syllabus.

ractical based on Syllabus.

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dective I (Introduction to Communication Systems)

BranchCode: EC-307

Periodic signals (Smusoidal, rectangular, saw tooth and triangular waves) and its Fourier series expansion with single side representation in real frequency domain and with double side representation in rotating phasor domain. Aperiodic signal: A signal pulse event and its fourier transform, impulse response of a linear Lecture: 4 time invariant system, convolution and response to arbitrary input. Block diagram of communication system and comparative study of analog and digital Lecture: 3 communication. Modulation (upward frequency translation) & demodulation (downward frequency translation) and the need for modulation, broad classification of modulation [linear (amplitude-AM) and Lecture: 6 exponential (frequency-FM and phase-PM)]. MODEM. Generation of double side band (DSB) with carrier, double side band with suppressed carrier (DSBSC) and single side band with suppressed carrier (SSBSC), demodulation of double side band with carrier- incoherent or envelope detector, peak diode detector, coherent or synchronous Lecture: 8 Jetection of DSBSC and SSBSC.

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Analog pulse modulation: PAM, PWM, PPM and demodulation, comparative study of various analog pulse modulation, comparison of incoherent and coherent detection. Lecture: 5 Superheterodyne Receivers: Intermediate frequency and its advantages, alignment and Lecture: 3 tracking, image rejection and IC version of the receiver. Lecture: 1 Frequency Multiplexing in carrier telephony. Generation of FM signals (direct and indirect methods) and demodulation. Lecture: 3 manative study of SNR in AM. FM. & PM system and use of emphasis circuit in FM for Lecture: 2 SNR optimization.

1 Television: Block diagram of the transmitter and receiver, description and working of video camera, description working of B/W, colour TV receiver, description of the composite signal in Lecture: 6 B/W, colour TV.

RADAR: Block diagram, operation and types, Radar range equation, Radar Transmitter & Lecture: 4 Perciver. Lecture: 2

← ← ← D Flat panel displays.

Padro systems for technicians by D C Green, Longman.

Learning electronic communication through experimentation using electronic work bench by Perube Pearson.

uces books

* summing attorn system by Bruce carison, TMII.

the trans communication system by Kennedy IV edition, TMH.

The treme communication system by Roody and Coolen, Pearson.

1-1- communication system engg. Vy Freeman, John Wiley.

communication system by Haykin, Wiley.

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Introduction to MOS technology: Introduction to IC technology, MOS & related VLSI technology, basic MOS transistors (Enhancement mode and depletion mode), NMOS process, CMOS process (P-well, N-well, Twin-tub processes), Bi CMOS process flow, aspects of CMOS & Bi CMOS devices.

Basic electrical properties of MOS circuits: MOSFET Threshold voltage, I-V relationship for MOSFET, MOSFET transconductance, the pass transistor, NMOS inverter, pull- up to pulldown ratio for NMOS inverter driven by NMOS inverter and pass transistor, different forms of oull-up (load resistor, depletion mode NMOS. Enhancement mode pull-up, CMOS pull-up), CMOS inverter, MOS transistor circuit model, latch up in CMOS circuits, Bi CMOS inverter, comparative aspects of CMOS and bipolar transistors.

MOS circuit design processes: MOS Lavers, stick diagrams (NMOS design style, CMOS design style), Euler path and design optimization, design rules and layout (Lambda based design rules, contact cuts, double metal MOS process rules, CMOS lambda based design rules), two Lecture: 5 micron double metal double poly CMOS rules.

Basic circuit concepts: Sheet resistance, area capacitance of layers, inverter delays, driving large capacitive loads, propagation delay (cascaded pass transistors, design of long poly silicon wires), wiring capacitances (fringing fields, interlayer capacitance, peripheral capacitance). Lecture: 6 Scaling of MOS circuits: Scaling models and scaling factors (gate area, gate capacitance, channel current density, channel resistance, gate delay, maximum operating frequency, saturation current, current density, power dissipation), limitation of scaling.

Subsystem design and layout: Switch logic (pass transistors and transmission gates), gate logic (inverter, two input CMOS NAND & NOR gates), structure design of a parity generator.

Lecture: 4

Term

7. Memory and aspects of system timing: System timing considerations, one transistor dynamic memory cell, three transistor dynamic RAM cell (area, dissipation, volatility), RAM arrays.

Practical aspects: Optimization of NMOS & CMOS inverters, input output pads, aspects of design tools (graphical and tree layout, design verification, design rule checkers, circuit Lecture: 4 extractors simulators).

Crystal growth and doping: Starting materials, Czochralski method, gradient freeze method, considerations for proper crystal growth (role of point defects, thermal gradients, turbulences, pull and spin rate, crystal orientation, crystal hardening techniques), doping (rapid stirring conditions, partial stirring conditions, radial doping variations), zone processes (zone refining, Lecture: 4 zone leveling, neutron transmutation doping).

10 Diffusion: Diffusion in a concentration gradient, diffusion equation, impurity behavior in silicon, diffusion systems for silicon, redistribution during oxide growth, diffusion during oxide growth, cooperative diffusion, evaluation techniques for diffused layers in silicon. Lecture: 2

II I pitary: Mucleation and growth, doping, dislocation, thermally induced strain, molecular beam Lecture: 2 epitasy vapour phase epitaxy for Si, liquid phase epitaxy.

12 Ion implantation: Penetration range (nuclear and electronic stopping, transverse effects), implantation damage, annealing, ion implantation systems, process consideration, high energy Lecture: 2 and high current implants.

Different types of microcontrollers: Embedded microcontrollers, external memory meteconneniers, processor architecture, Harvard & Princeton. Microcontrollers: Overview of 8051 microcontroller, application areas, compares and contrasts. 8051 Microcontrollers architecture: 8051 pin description, conception about program counter, Alicioprocessor and microcontroller. data pointer register bank, flags, program status word (PSW), internal memory. RAM, ROM, memory map, stack and stack pointer, input and output ports, external memory, counters and timers, serial data, input/output interrupts. 8051 addressing modes: Immediate and register addressing modes. Accessing memory using various addressing modes. Bit address for LO and RAM. Assembling and running an 8051 program, 8051assembly language programming concepts using arithmetic, logical, data mover, call, jump, loop, time delay instructions and subroutines.

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. 1/O port programming: 8051 1/O port assembly language programming concepts. Lecture: 3

Assembly language programs based on rotate, compare and data serialization concepts.

Lecture: 3

- 8. 8051 timers programming in assembly: 8051 timers programming concept, counter Lecture: 3
- 2. Anterrupts programming in assembly: Programming timer interrupts.

10. Real world interfacing on 8051: Intelligent LCD display, interfacing keyboard to 8051. Lecture: 5

11 PIC microcontrollers: Introduction to PIC microcontrollers, architecture and pipelining, program memory considerations. Addressing modes. CPU registers, instruction set, simple operation

ooks:

- The 8051 microcontroller and embedded system by M A Mazidi, Pearson/PHI.
- L. Deagn with PIC microcontrollers by John B Peatman, Pearson.

mee books:

- 1 Une 8051 Microcontroller by Kenneth Ayala, Thomson learning.
- Linbudded microcontroller systems by J W Valvano Brooks, Thomson learning.

1. And the controllers and microcomputers by Fredrick M Cady, Oxford.

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Term

Introduction: Background, digital image representation, fundamentals step in image processing, elements of a digital image processing system.

Lecture:4

2. Digital image fundamentals: Elements of visual perception, a simple image model, sampling and quantization, some basic relationship between pixels, imaging geometry.

Lecture:6

Image transforms: introduction to the fourier transform, the discrete fourier transform, some properties of the two-dimensional fourier transform, other separable image transforms.

4. Image enhancement: Spatial domain methods, frequency domain methods, some simple intensity transformations, histogram processing, image subtraction, image averaging, background, smoothing filters, sharpening filters, low pass filtering, high pass filtering, generation of spatial masks from frequency domain specification.

Lecture:9

nd Tarm

5. Image restoring: Degradations model- definitions, degradation model for continuous functions, diagonalization of the circulant and the block circulant matrices, circulant matrices, block circulant matrices, effects of diagonalization on the degradation model, algebraic approach to restoration, unconstrained restoration, constrained restoration, inverse filtering- formulation, removal of blur caused by uniform linear motion, restoration in the spatial domain, geometric transformation.
Lecture:12

6. Image compression: Fundamentals- coding redundancy, interpixel redundancy, psycovisual redundancy, fidelity criteria, image compression models- the source encoder and decoder, the channel encoder and decoder. Elements of information theory- measuring information, the information channel, fundamental coding theorems, using information theory. Error- free compression- variable- langth coding, bit- plane coding, lossless predictive coding. Lossy compression- lossy predictive coding, transform coding.

Lecture:14

t books:

1. Digital image processing, 2nd edition by Rafael C Gonzalez and Richard E Woods, Pearson.

erence books:

1. Digital image processing by W K Pratt, John Wiley.

2. Fundamentals of digital image processing by A K Jain, PHI.

3. Image processing, analysis and machine visions by M Sonka et. al., Thomson Learning, Indian

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Branch Code: EC-308 : Elective-II (Fiber Optic Communication) (ECE) Historical note of comparative study with respect to RF and microwave communication. Lecture: 2 Lecture: 2 Block diagram of an optical fiber communication system. Optical fiber: Basic optical laws and definitions, principles of light propagation in fibres, ray dicory, optical fiber materials, fiber fabrication, optical fiber cables. Signal degradation in optical fibers: Attenuation, chromatic dispersion and inter modul dispersion, dispersion shifted and flattened fibers. Optical modulator: LED & LASER diodes- basic concepts, operation, modulation methods, Optical detector: PIN & APD-operation, detector noise, response time, photodiode materials. Lecture: 8 Lecture: 5

7. Splices, connectors, couplers and grating

8. Optical Transmitters.

9. Optical Receivers.

10. Optical link design.

Lecture: 5

Lecture: 6

1. Fiber optic system by John Powers, Irwin.

ner books:

- 1. Optical fiber communication by Keiser, TMH.
- 2. Optical fiber communication by Senior, PHI.
- 3. Optical communication systems by Gowar, PHI.
- 4 Opto Flectronics- an introduction by Wilson & Hawkes, PHI. and paragrammunication by Palais, Pearson.

Optic Communication Lab:

- Is Modulation and demodulation using fiber optic cable.
- 2. Wavelength measurement using LASER.
- 1. Measurement of attenuation losses in FOC.
- 4. To determine the wavelength of laser light with a transmission grating.

2 37113 RO MINITY 17/11/13

Introduction to Computer aided design tolls for digital systems. Hardware description languages, introduction to VHDL, data objects, classes and data types, operators, overloading, logical operators. Types of delay entity and architecture declaration. Introduction to behavioural. Lecture: 12 Data flow and structure models.

Assignment statements: Sequential statements and process, conditional statements, case statement. Array and loops, resolution functions, packages and libraries, concurrent statements. Subprograms: application of functions and procedures, structural modeling. Declaration Lecture: 12 structural layout and generics.

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VHDL Models and simulation of combinational circuits such as multiplexers, demultiplexers, encoders, decoders, code converters, comparators, implementation of Boolean functions etc. VHDL models and simulations of sequential circuits shift registers, Counters etc.

Lecture: 8

Basic components of a computer: Specifications, architecture of a simple microcomputer system, implementation of a simple microcomputer system using VHDL. Lecture: 8 Programmable logic devices: ROM. PLAs, PALs, GAL, CPLDs, and FPGA, design Lecture: 8 implementation using CPLDs and FPGAs.

e books:

IEEE standard VHDL language reference manual (1993). Digital design and modeling with VHDL and synthesis by K C Chang, IEEE computer society press.

A VHDL primer by Bhaskar, Prentice Hall(1995).

Digital system design using VHDL by Charles H Roth, PWS (1998).

VHDL analysis and modeling of digital systems by Nawabi Z, McGraw Hill.

VHDL 4th edition by Perry, TMH.

Introduction of digital systems by Ereegovac, Lang & Moreno, John Wiley.

Fundamentals of digital logic with VHDL design by Brown & Vranesic, TMH.

Modern Digital Electronics 3rd edition by R P Jain.

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ntroduction: Embedded systems overview, processor technology- General purpose processor single purpose processors (hardware), Application- specific processors, IC cehnology- full-custom/VLSI, Semicustom ASCI (gate array and standard cell), PLD, etc. Lecture: 5

Basic concepts of computer architecture: Concepts, memory, I/O, DMA, parallel and distributed computers, embedded computers architecture, etc. Embedded processors and systems: The PIC Micro-controllers- A table of two processors and support components, Bus interfacing, AT 90S8515 Memory cycle and Bus signals, memory maps and address decoding, programmable logic (PALs, LCAs or PLDs), timing analysis and memory management.

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MC 68000 Series computer: A simple 68000 architecture, a simple 68000 based computer reset circuit, address decoder I/O (Multifunction peripheral), memory interfacing to SRAM and EPROMs, wait state generator, etc.

DSP based controller: DSP 56800 programmer's model, a DSP 56805 based computer DSP 56805 block diagram, crystal oscillator circuit and module, reset and interrupts, external memory, interfacing to program, SRAM and data SRAM, shared program and data memory, address decoder for two 32k SRAMs and eight peripherals, JTAG.

Peripherals and interfacing: Adding peripherals and interfacing- serial peripherals and interfacing- serial peripheral interface (SPI), Inter Integrated circuit (I²C), adding a real time clock with I²C, adding a small display with I²C, Serial ports- UARTs, Error detection, RS- 232c, & RS-422, Infrared communication, USB networks- RS-485, Controller area network (CAN), Ethernet, Analog sensors- Interfacing external ADC, Temperature sensor, light sensor, accelerometer, pressure sensor, magnetic field sensor, DAC, PWM, embedded system applications- motor control and switching big loads.

1. Designing embedded hardware by Catsoulis, Hohn, Shroff Pub. and distributors pvt. Ltd. New

2. Embedded system design- A unified hardware/software introduction by Vahid, Frank and Givargis, Tony, John Wiley and sons, Peplika presq Delhi. 100113 Paris Vivenia 1711113 VIVENIA 1711113 VIVENIA 1711113